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PATENT
Attorney Docket No.: SONY-12200

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

David V. James et al.

Serial No.: 09/611,766

Filed: July 6, 2000

For: **METHOD OF AND APPARATUS
FOR DIRECTLY MAPPING
COMMUNICATIONS THROUGH
A ROUTER BETWEEN NODES ON
DIFFERENT BUSES WITHIN A
NETWORK OF BUSES**

) Group Art Unit: 2731

) Examiner:

) **TRANSMITTAL LETTER**

) 260 Sheridan Avenue, Suite 420
) Palo Alto, CA 94306
) (650) 833-0160

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Technology Center 2600

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Enclosed please find a Supplemental Information Disclosure Statement, Form PTO-1449, including copies of the references contained thereon, for filing in the U.S. Patent and Trademark Office.

The Commissioner is hereby authorized to charge any additional fee or credit overpayment to our Deposit Account No. 08-1275. **An originally executed duplicate of this transmittal is enclosed for this purpose.**

Respectfully submitted,
HAVERSTOCK & OWENS LLP

Dated: July 26, 2001

By: Jonathan O. Owens
Jonathan O. Owens
Reg. No.: 37,902

CERTIFICATE OF MAILING (37 CFR § 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Assistant Commissioner for Patents, Washington D.C. 20231

Attorneys for Applicants

HAVERSTOCK & OWENS LLP

Date: 7/26/01 By: [Signature]



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Examiner:

SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT

260 Sheridan Avenue, Suite 420
Palo Alto, California 94306
(650)833-0160

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The citations listed below, copies attached, may be material to the examination of the above-identified application, and are therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. §§ 1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

Applicant has become aware of the following printed publication which may be material to the examination of this application:

- U.S. Patent No. 5,402,419;
- U.S. Patent No. 5,485,458;
- U.S. Patent No. 5,497,466;
- U.S. Patent No. 5,499,344;
- U.S. Patent No. 5,500,946;
- U.S. Patent No. 5,594,734;
- U.S. Patent No. 5,602,841;
- U.S. Patent No. 5,617,419;
- U.S. Patent No. 5,623,610;

- U.S. Patent No. 5,664,124;
- Michael Teener et al., "A Bus on a Diet - The Serial Bus Alternative, An Introduction to the P1394 High performance Serial Bus" Apple Computer, Inc. Santa Clara, CA, Pub. Date.: 02/24/92, pgs. 316-321;
- P1394 Standard For A High Performance Serial Bus," IEEE, 1995;
- "The IEEE-1394 High Speed Serial Bus," R.H.J. Bloks, Philips Journal Of Research, Vol.50, No. 1/2, pp. 209-216, 1996;
- SPI, "Architecture for High Performance Transparent Bridges" July, 1992, IBM Corporation, 3 pages; and
- "Digital Interface for Consumer Audio/Video Equipment" The Netherlands, 46 pages.

This Supplemental Information Disclosure Statement under 37 C.F.R. §§ 1.56 and 1.97 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that anyone or more of these citations constitutes prior art.

Respectfully submitted,
HAVERSTOCK & OWENS LLP

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